



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/668,254	09/24/2003	Yasuji Norito	K06-159164M/AT	3287
21254	7590	01/12/2005	EXAMINER	
MCGINN & GIBB, PLLC 8321 OLD COURTHOUSE ROAD SUITE 200 VIENNA, VA 22182-3817				LAU, TUNG S
		ART UNIT		PAPER NUMBER
		2863		

DATE MAILED: 01/12/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	10/668,254	NORITO, YASUJI
	Examiner Tung S Lau	Art Unit 2863

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 24 September 2003.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-10 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) 3-10 is/are allowed.
- 6) Claim(s) 2 is/are rejected.
- 7) Claim(s) 1 is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 31 October 2003 is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) All b) Some * c) None of:
1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ . |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>See office action</u> . | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| | 6) <input type="checkbox"/> Other: _____ . |

DETAILED ACTION

Information Disclosure Statement

1. The information disclosure statement filed 2-5-2004 fails to comply with 37 CFR 1.98(a)(2), which requires a legible copy; each publication or that portion which caused it to be listed; and all other information or that portion which caused it to be listed. Item France 2817970, European search report Jan 14, 2004 and voltage comparator circuit, IBM Vol. 17. no. 4, Sept. 1974, pp1151-1152 are missing from the application file. Applicant is required to submit a legible copy of France 2817970, European search report Jan 14, 2004 and voltage comparator circuit, IBM Vol. 17. no. 4, Sept. 1974, pp1151-1152.

The IDS filed on 9-24-2003 has been accepted and signed by the examiner.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Davis (U.S. Patent 6,134,655).

Regarding claim 1:

Davis discloses a device for setting a hysteresis characteristic with respect to an input signal, said device comprising: a voltage dividing circuit for dividing a voltage said input signal into a first voltage and a second voltage which is lower

than said first voltage (fig. 3, unit q14, q9); and a computer including a first port to which said first voltage is given (fig. 3, unit p1, p2), and a second port to which said second voltage is given, said computer performing a predetermined software process to set a hysteresis characteristic (fig. 3, unit p1, p2); wherein said computer performs a software process when said voltages given said first and second ports are equal to or higher than a predetermined threshold (fig. 3, unit p1, p2), determining said input signal to have a high level; when said voltage given to said first port is equal to or higher than the threshold and said voltage given to said second port is lower than the threshold (fig. 3, unit p1, p2), making a same determination as a immediately preceding determination; and when said voltages given to said first and second ports are lower than the predetermined threshold, determining said input signal to have a low level (fig. 3, unit p1, p2).

Claim Objections

3. Claims 2 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all the limitation of the base claim and any intervening claims.

The following is an examiner's statement of reasons for allowance: prior art fail to teach voltage divider circuit includes first and second resistors which are connected to each other in series.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Allowable Subject Matter

4. Claims 3-10 are allowed.

Reasons for Allowance

5. The following is an examiner's statement of reasons for allowance:

Independent claims 3 and 7 contain allowable subject matter. None of the prior art of record shows or fairly suggests the claimed invention.

Regarding claim 3:

The primary reason for the allowance of claim 3 is a device for setting a hysteresis characteristic with respect to an input signal, said device including a second comparator for comparing second signal with predetermined threshold. It is these features found in the claim, as they are claimed in the combination, that has not been found, taught or suggested by the prior art of record which makes this claim allowable over the prior art.

Claims 4-6 are allowed due to their dependency on claim 3.

Regarding claim 7:

The primary reason for the allowance of claim 7 is a method for setting hysteresis characteristic in respect to an input signal, said method including hysteresis characteristic of said input signal defined as same as an immediately preceding output signal in case that said first signal is equal to or higher than said predetermined threshold in result said first comparing step and said second signal is lower than said predetermined threshold in result of said second comparing step. It is these features found in the claim, as they are claimed in the combination, that has not been found, taught or suggested by the prior art of record which makes this claim allowable over the prior art.

Claims 8-10 are allowed due to their dependency on claim 7.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tung S Lau whose telephone number is 571-272-2274. The examiner can normally be reached on M-F 9-5:30. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Barlow can be reached on 571-272-2269. The fax phone numbers for the organization where this application or proceeding is assigned is 703-872-9306

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TL

John Barlow
John Barlow
Supervisory Patent Examiner
Technology Center 2800